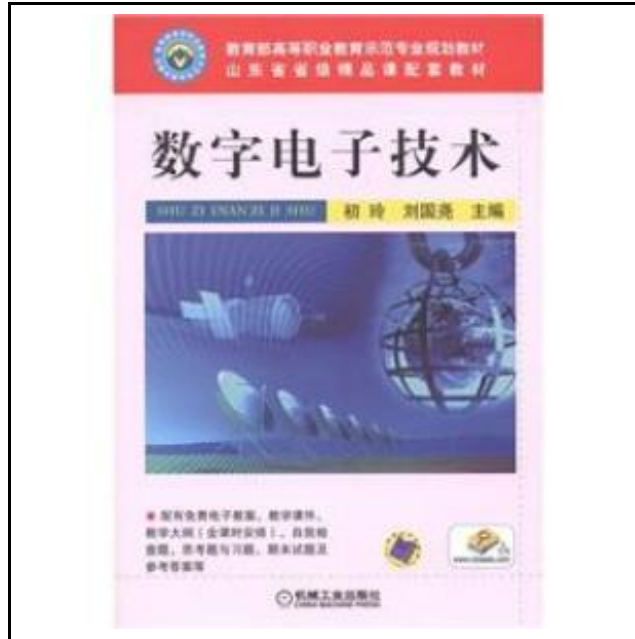# Digital electronic technology (Ministry of Education. higher vocational education model professional planning materials)

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DIGITAL ELECTRONIC TECHNOLOGY (MINISTRY OF EDUCATION. HIGHER VOCATIONAL EDUCATION MODEL PROFESSIONAL PLANNING MATERIALS)

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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 229 Publisher: Machinery Industry Pub. Date :2011-09-01 version 1. The beginning of Ling and Liu Guoyao editor of the digital electronics is divided into nine chapters. the main contents include: the basics of digital circuits. digital logic based . logic gates. combinational logic circuits. flip-flops and application integration. sequential logic circuits. pulse generation and shaping. DA and AD conversion and semiconductor memory and programmable logic devices. Digital Electronics as ordinary colleges. vocational schools of electronic information. computer classes. communication classes. Automation and specialty electrical and mechanical equipment or reference materials. also available in related fields of technology and management to learn information. Contents: Preface Chapter 1. the basics of digital circuit digital circuit 1.1 1.1.2 Overview 1.1.1 pulse signal pulse parameters 1.2 system and the code number system number system 1.2.2 1.2.1 conversion between different number systems 1.2.3 code 1.3 RC circuit and system applications 1.3.1 simple RC circuit analysis methods 1.3.2 RC circuit charge and discharge process 1.4 Application 1.3.3 RC circuit switching characteristics of diode switching characteristics of transistors 1.5 Summary of self-examination questions and problems thinking exercises Chapter 2 2.1 digital logic-based logic variable logic functions and logic functions 2.1.1 Definition 2.1.2 logic function representation of the basic logic operations and rules 2.2 2.2.1 2.2.2 basic logical operations logical formula. rule 2.3 logic function 2.4 Simplification of the formula method of Karnaugh map logic function method 2.4.2 Simplification 2.4.1 Karnaugh map simplification with Karnaugh map logic functions 2.5 hardware description language VHDL and logic simulation 2.5.1 hardware description language VHDL 2.5.2 Summary of self-checking logic simulation questions and problems thinking exercises in Chapter 3 are three basic logic gates logic 3.1 3.2 3.3 composite...

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